AMENDMENTS TO THE SPECIFICATION:

In the related application section of the subject application, please replace the second full paragraph [0002] with the following amended paragraph:

[0002]   "CACHE COHERENCY PROTOCOL WITH ORDERING POINTS," Application Ser. No. 10/760,640; "SYSTEM AND METHOD FOR RESOLVING TRANSACTIONS IN A CACHE COHERENCY PROTOCOL," Application Ser. No. 10/760,813; "SYSTEM AND METHOD TO FACILITATE ORDERING POINT MIGRATION," Application Ser. No. 10/761,048; "SYSTEM AND METHOD TO FACILITATE ORDERING POINT MIGRATION TO MEMORY," Application Ser. No. 10/760,599; "SYSTEM AND METHOD FOR CREATING ORDERING POINTS," Application Ser. No. 10/760,652; "SYSTEM AND METHOD FOR CONFLICT RESPONSES IN A CACHE COHERENCY PROTOCOL WITH ORDERING POINT MIGRATION," Application Ser. No. 10/760,651, which issued as U.S. Patent No. 7,359,374 on June 11, 2008; "SYSTEM AND METHOD FOR READ MIGRATORY OPTIMIZATION IN A CACHE COHERENCY PROTOCOL," Application Ser. No. 10/761,044, which issued as U.S. Patent No. 7,143,245 on November 28, 2006; "SYSTEM AND METHOD FOR BLOCKING DATA RESPONSES," Application Ser. No. 10/761,034, which issued as U.S. Patent No. 7,149,852 on December 12, 2006; "SYSTEM AND METHOD FOR NON-MIGRATORY REQUESTS IN A CACHE COHERENCY PROTOCOL," Application Ser. No. 10/760,659; "SYSTEM AND METHOD FOR CONFLICT RESPONSES IN A CACHE COHERENCY PROTOCOL WITH ORDERING POINT MIGRATION," Application Ser. No. 10/761,073; "SYSTEM AND METHOD FOR RESPONSES BETWEEN DIFFERENT CACHE COHERENCY PROTOCOLS," Application Ser. No. 10/760,436, which issued as U.S. Patent No. 7,177,987 on February 13, 2007, all of which are filed contemporaneously herewith on January 20, 2004, and are incorporated herein by reference.